

Notice for RET in Electronics for PhD Admission 2017

RET - Written Examination for admission to PhD course of the Department of Electronics will be held on 25-05-2017 at 12 noon.

There will be one paper of 100 marks with 50 MCQ questions. Group-A will contain 20 questions and Group-B will contain 30 questions. 1 hour time will be given for written test.

Syllabus: Group-A: As per UGC-NET-Paper-I

Group-B: As per UGC-NET-Electronics Science Paper-II & III

Result of RET will be displayed in departmental notice board on 29/05/2017.

Interview date: 30/05/2017.

**Sd/-
BRS-Electronics, Convenor
Dr. Arijit Roy**